

PATENT, TRADEMARK & COPYRIGHTS

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June 16, 2004

Certificate

JUN 24 2004

of Correction

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Re.: Inventor: Shigeto Igarashi
Patent No.: 6,618,080
Issued: September 9, 2003
Serial No.: 09/491,581
Filed: January 25, 2000
For: AUXILIARY AMPLIFIER SELECTION
CIRCUIT FOR A CCD CAMERA
Atty Docket No.: 2811

#16
Sgn

Sir:

Enclosed herewith for filing is a Certificate of Correction of Patent and Trademark Office mistakes under Rule 322 & 323 (35 U.S.G. 254) in the above identified patent. All the corrections are for errors made by the Patent and Trademark Office during printing of the application and there should be no fee.

Approval and entry of the enclosed Certificate of Correction is earnestly solicited.

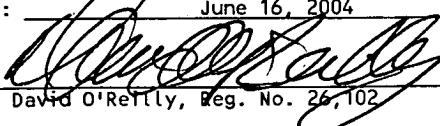
Respectfully submitted,


David O'Reilly
Registration No. 26,102

DO'R/en
Enclosures

25 JUN 2004

I hereby certify that this correspondence is being deposited with the U. S. Postal Service as first class mail, postage prepaid, under 37 CFR 1.10 in an envelope addressed to Commissioner of Patents, Box 1450, Alexandria, VA 22313-1450.
Date: June 16, 2004

By: 
David O'Reilly, Reg. No. 26,102

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,618,080

DATED : September 9, 2003

INVENTOR(S) : Shigeto Igarashi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 1, line 29, begin new paragraph at "Operation of above device is as follows:";

line 64, delete "S/N.ratio" and insert "S/N ratio";

Col. 3, line 32 $\frac{1}{2}$, after "Second Embodiment." insert new paragraph "Fig. 3 shows the second embodiment of this invention.";

line 42, after "amplifying circuit" insert " m_2 ";

line 46, delete "highf S/N" and insert "high S/N".

MAILING ADDRESS OF SENDER:

David O'Reilly
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PATENT NO. 6,618,080

No. of additional copies



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,618,080 B1
DATED : September 9, 2003.
INVENTOR(S) : Shigeto Igarashi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1:

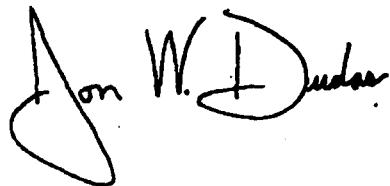
Line 29, begin new paragraph at "Operation of above device is as follows:";
Line 64, delete "S/N.ratio" and insert -- S/N ratio --.

Column 3:

Line 32½, after "Second Embodiment." insert new paragraph -- Fig. 3 shows the second embodiment of this invention. --;
Line 42, after "amplifying circuit" insert -- m₂ --;
Line 46, delete "highf S/N" and insert -- high S/N --.

Signed and Sealed this

Twenty-seventh Day of July, 2004



JON W. DUDAS
Acting Director of the United States Patent and Trademark Office

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,618,080 *b1*
DATED : September 9, 2003
INVENTOR(S) : Shigeto Igarashi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 1, line 29, begin new paragraph at "Operation of above device is as follows:"; *C*

line 64, delete "S/N.ratio" and insert "S/N ratio"; *C*

Col. 3, line 32 $\frac{1}{2}$, after "Second Embodiment." insert new paragraph "Fig. 3 shows the second embodiment of this invention."; *C*

line 42, after "amplifying circuit" insert " m_2 "; *C*

line 46, delete "highf S/N" and insert "high S/N". *C*

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PATENT NO. 6,618,080

No. of additional copies



25 JUN 2004